Notice of Allowability	Application No.	Applicant(s)	Applicant(s)	
	09/699,947	KELLY ET AL.		
	Examiner	Art Unit		
	Tuan V. Thai	2186		
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comm IGHTS. This application is	in this application. If not include nunication will be mailed in due	ed course. THIS	
1. $\boxtimes$ This communication is responsive to <u>amendment filed 12/</u>	<u>12/2006</u> .			
2. 🔀 The allowed claim(s) is/are <u>1-3, 5, 18 and 20 renumbered</u>	as 1-6 respectively.			
3. ☐ Acknowledgment is made of a claim for foreign priority unallocation of the:		or (f).		
1. Certified copies of the priority documents have				
2. Certified copies of the priority documents have				
3. Copies of the certified copies of the priority do	cuments have been receive	ed in this national stage applicat	ion from the	
International Bureau (PCT Rule 17.2(a)).				
* Certified copies not received:				
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to fil IENT of this application.	e a reply complying with the rec	uirements	
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give</li> </ol>	nitted. Note the attached EX es reason(s) why the oath o	AMINER'S AMENDMENT or Nor declaration is deficient.	OTICE OF	
5. CORRECTED DRAWINGS ( as "replacement sheets") mus				
<ul><li>(a) ☐ including changes required by the Notice of Draftspers</li></ul>	son's Patent Drawing Revie	w ( PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date	,			
(b) ☐ including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment o	or in the Office action of		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the header according to 37 C	the drawings in the front (not the FR 1.121(d).	back) of	
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT</li> </ol>	SIT OF BIOLOGICAL MAT FOR THE DEPOSIT OF BI	ERIAL must be submitted. NOLOGICAL MATERIAL.	lote the	
Attachment(s)  1. □ Notice of References Cited (PTO-892)  2. □ Notice of Draftperson's Patent Drawing Review (PTO-948)	_	nformal Patent Application Summary (PTO-413),		
	Paper No.	No./Mail Date		
<ol> <li>Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date</li> </ol>	7. ∐ Examiner's	☐ Examiner's Amendment/Comment		
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's	's Statement of Reasons for Allowance		
	9. 🗍 Other	Landa TUAN V. THAI PRIMARY EXAMI	NER	
U.S. Patent and Trademark Office PTOL-37 (Rev. 08-06)	Notice of Allowability		ail Date 20070107	

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Attorney's Docket No.: TRANS04D

## IN THE UNITED STATES PATENT AND

## TRADEMARK OFFICE

In re application of: Kelly et al. Group: 2186

Serial No.: 09/699,947 Examiner: Tuan Thai

For: TRANSLATED MEMORY PROTECTION APPARATUS FOR AN ADVANCED MICROPROCESSOR.

1. This action is responsive to amendment filed December 12, 2006. Claims 1-3, 5, 18 and 20 are now allowed. Claims 4, 6-17 and 19 have been canceled.

## REASONS FOR ALLOWANCE

2. The following is an Examiner's Statement of Reasons for Allowance:

The prior arts of record do not teach nor suggest, either alone or in combination, all the limitations of the claimed invention (claims 1 and 18) particularly a method and system for maintain translation consistency in a computer having a processor for executing instructions of a host instruction set and software for translating instructions from a target instruction set to instructions of the host instruction set

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comprises hardware means for indicating whether a memory address to be written stores a target instruction that has been translated to host instruction, and software means which responding to an indication that a memory address to be written stores a target instruction that has been translated to host instruction for assuring that host instructions translated from target instructions stored at the memory address not to be used when the memory address has been written, the software means removes the at least one host instruction from the second memory address; and wherein the hardware means further comprises a lookaside buffer including a plurality of storage locations for virtual addresses and associated physical addresses, and a storage position in each storage location of the translation look-aside buffer.

Claims 2-3, 5 and 20 further limit the allowable independent claims. These claims are therefore allowable for the same reason as set forth above.

Any comments considered necessary by Applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier

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communications from the examiner should be directed to Tuan V.

Thai whose telephone number is (571)-272-4187. The examiner can normally be reached on from 6:30 A.M. to 4:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew M. Kim can be reached on (571)-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

**TVT**/January 06, 2007

/ PRIMARY EXAMINER

Group 2100